HW

Describe how the DAC below works. For two samples of switch configuration (on-off-off-off-off and off-off-on-on), find the output voltage. Assume that the inverting input of the op-amp is at the ground level.

![DAC Diagram]

\[ V_{\text{ref}} \]

\[ 2R \quad 2R \quad 2R \quad 2R \quad R \]

\[ R \quad R \quad R \quad R \]

\[ + \]

\[ - \]